Joseph **DeVictoria**

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Experience

SK Hynix NAND America — *Rancho Cordova, CA*
SENIOR STAFF PRODUCT ENGINEER — *November 2023 – Present*
- Interim leader of nine person Wafer Test (Sort) team from May 2024 to October 2024.
- Wafer test software development and pathfinding on Advantest platform.
- Initial silicon bring-up, debug, and validation for new 3D NAND products.
- Planning and project management for all wafer test (Sort) deliverables.

Intel Corporation — *Folsom, CA*
SENIOR PRODUCT DEVELOPMENT ENGINEER (G7) — *March 2018 – November 2023 - 5.7 Years*
- Development of production wafer ATE software for testing new 3D NAND memory products.
- Implementation of generational improvements in C++ and APG code architecture. (Terradyne Magnum 5)
- Iterative development of tester platforms, tester-code interface, probe cards, and new engineering hardware.
- Expertise in NAND memory architecture, test modes, customer modes, and storage system concepts.
- Python and Go programming for data analysis and engineering validation tools.

Micron Technology — *Folsom, CA*
NAND PRODUCT ENGINEER (E2) — *May 2015 – March 2018 - 2.8 Years*
- Bench level validation of test algorithms using scripted test platform, oscilloscopes, and logic analyzers.
- Pathfinding for new tests and technical documentation of test changes across product generations.
- Digital and analog simulations to verify correct test sequences and product behavior at the model level.
- Yield and performance tracking across test releases for qualification and high volume manufacturing.
- Tactical support of code development teams to remove roadblocks for production test releases.

Fusion-io — *Salt Lake City, UT*
HARDWARE ENGINEERING INTERN — *April 2014 – August 2014 - 4 Months*
- Inspection, repair, bring-up, and regression testing for prototype enterprise SSDs.
- Standardized lab operating system images for quickly deploying test servers for addressing RMAs..
- C and Python development for internal SSD power monitoring tools.

Brigham Young University — *Provo, UT*
COMPUTER TECHNICIAN — *August 2012 – April 2015 - 3 Years*
- Deployed and maintained over one thousand Windows and Macintosh workstations in the Harold B. Lee Library.
- Provided one-on-one support for both software and hardware problems for library employees and patrons.
- Administered regular LTO tape backups of enterprise share drives and employee PC files.

Education

Brigham Young University — *Provo, UT*
BS IN COMPUTER ENGINEERING — *April 2015*

Brigham Young University — *Provo, UT*
MINOR IN COMPUTER SCIENCE — *April 2015*

Skills

Programming Languages: C++, Go, Python, C, Java, Perl, Javascript, Verilog, Assembly, and APG languages
Engineering Techniques: Pathfinding, Prototyping, Debugging, Data Analysis, Tool Development, and Validation
Lab Equipment: Servers (Linux/BSD/Windows), ATE, Probers, Logic Analyzers, and Oscilloscopes