Joseph **DeVictoria**

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Experience

Solidigm — *Rancho Cordova, CA*
STAFF PRODUCT ENGINEER (G8) - SORT — *April 2025 – Present*
- Lead read window budget (RWB) cross-platform correlation efforts.
- Delivered an automated testing system for providing small-batch 3D NAND engineering samples.
- Fixed hardware issues gating the bring-up of the Product Engineering lab tools, un-gating critical downstream development.

SK Hynix America — *Rancho Cordova, CA*
SENIOR STAFF PRODUCT ENGINEER (L7) - WAFER TEST — *November 2023 – April 2025 - 1.4 Years*
- Directed a nine person Wafer Test development team during a critical six month period as the interim manager.
- Delivered functional wafer test software on Advantest platform including new test algorithms and test time reduction.
- Conducted the initial silicon bring-up, debug, and validation for new 3D NAND products.
- Planned and tracked team deliverables, working with senior management to mitigate roadblocks and headwinds.

Intel Corporation — *Folsom, CA*
SENIOR PRODUCT DEVELOPMENT ENGINEER (G7) - SORT — *March 2018 – November 2023 - 5.7 Years*
- Developed production wafer ATE software for testing new 3D NAND memory products.
- Implemented generational improvements in C++ and APG code architecture on the Teradyne Magnum V platform.
- Delivered time-critical probe card designs and validation for new manufacturing and engineering hardware.
- Mentored others as a team expert in NAND memory architecture and features, test modes, and storage system concepts.
- Developed several Python and Go tools for data analysis and engineering validation.

Micron Technology — *Folsom, CA*
NAND PRODUCT ENGINEER (E2) - PROBE & BURN — *May 2015 – March 2018 - 2.8 Years*
- Validated 3D NAND test algorithms and conditions using a scriptable bench test platform, oscilloscopes, and logic analyzers.
- Conducted pathfinding for new tests and provided technical documentation of test changes across product generations.
- Performed digital and analog simulations to verify correct test sequences and product behavior at the model level.
- Provided tactical support to code development teams for removing roadblocks that prevent production test releases.
- Tracked yield and performance across test releases to prepare for product qualification.

Fusion-io — *Salt Lake City, UT*
HARDWARE ENGINEERING INTERN - SSD — *April 2014 – August 2014 - 4 Months*
- Inspection, repair, bring-up, and regression testing for prototype enterprise SSDs.
- Standardized lab operating system images for quickly deploying test servers to address RMAs.
- Developed C and Python software for internal SSD power monitoring tools.

Brigham Young University — *Provo, UT*
COMPUTER TECHNICIAN - LIBRARY — *August 2012 – April 2015 - 3 Years*
- Deployed and maintained over one thousand Windows and Macintosh workstations in the university library.

Education

Brigham Young University — *Provo, UT*
BS IN COMPUTER ENGINEERING — *April 2015*

Brigham Young University — *Provo, UT*
MINOR IN COMPUTER SCIENCE — *April 2015*

Skills

Programming Languages: C++, Go, Python, C, Assembly, APG Pattern Languages (ATL, etc.)
Engineering Techniques: Silicon Bring-up, ATE Development, Pathfinding, Prototyping, Debugging, Data Analysis, Tools
Lab Equipment: Servers, ATE, Wafer Probers, Logic Analyzers, and Oscilloscopes